

# DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

## APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING CONDITION IN MULTISTATE MEMORY DEVICE

the specification of which (check one) ☒ is attached hereto or was filed on \_\_\_\_\_ as Application Serial No. \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
			Yes	No
Number	Country	Day/Month/Year Filed		
Number	Country	Day/Month/Year Filed		

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Ser. No.	Filing Date	Status: Patented, Pending, Abandoned
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor ROBERT D. NORMAN

Inventor's signature Robert D. Norman 10/21/96  
Date

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Full name of second joint inventor, if any, CHRISTOPHE J. CHEVALLIER

Inventor's signature Christophe J. Chevallier Oct 21, 96  
Date

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Post Office Address 168 Tennyson Avenue, Palo Alto, CA 94301

**S/N 09/031,391**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Robert D. Norman et al.	Examiner:	
Serial No.:	09/031,391	Group Art Unit:	2785
Filed:	February 26, 1998	Docket:	7Q3.079US2
Title:	APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING CONDITION IN MULTISTATE MEMORY DEVICE		

**REVOCATION AND POWER OF ATTORNEY**

Assistant Commissioner for Patents  
Washington, D.C. 20231

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05, 402.07, and 324 please revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith:

Anglin, J. Michael	Reg. No. 24,916	Holloway, Sheryl S.	Reg. No. 37,850
Arora, Suneel	Reg. No. P-42,267	Huebsch, Joseph C.	Reg. No. P-42,673
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Drake, Eduardo E.	Reg. No. 40,594	Lundberg, Steven W.	Reg. No. 30,568
Dryja, Michael A.	Reg. No. 39,662	Lynch, Michael L.	Reg. No. 30,871
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Farney, W. Bryan	Reg. No. 32,651	Pappas, Lia M.	Reg. No. 34,095
Fogg, David N.	Reg. No. 35,138	Schwegman, Micheal L.	Reg. No. 25,816
Forrest, Bradley A.	Reg. No. 30,837	Slifer, Russell D.	Reg. No. 39,838
Hale, Jeffrey D.	Reg. No. 40,012	Terry, Kathleen R.	Reg. No. 31,884
Harris, Robert J.	Reg. No. 37,346	Viksuins, Ann S.	Reg. No. 37,748
Hofmann, Rudolph P., Jr.	Reg. No. 38,187	Woessner, Warren D.	Reg. No. 30,440

**CERTIFICATE UNDER 37 CFR §3.73(b) CERTIFICATE UNDER 37 CFR §3.73(b)**

Micron Technology, Inc. hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed October 21, 1996 and recorded on Reel 8281, Frames 0931. To the best of my knowledge and belief, title is in Micron Technology, Inc., the assignee.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Michael L. Lynch, am empowered to sign this certificate on behalf of Micron Technology, Inc., the assignee.

Please note that Micron Quantum Devices, Inc. has merged into Micron Technology, Inc. as evidenced by the attached Certificate of Merger.

I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

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Date

8/25/98

By

Michael L. Lynch

Title: Chief Patent Counsel